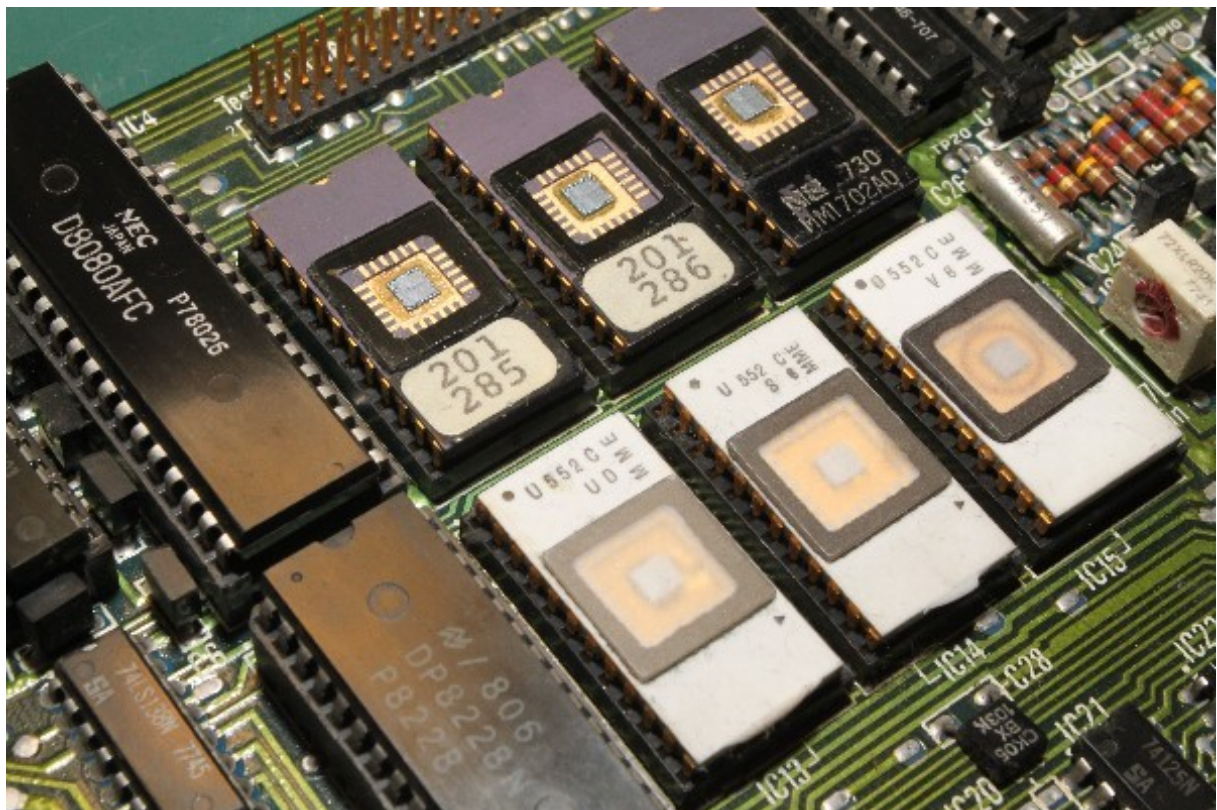


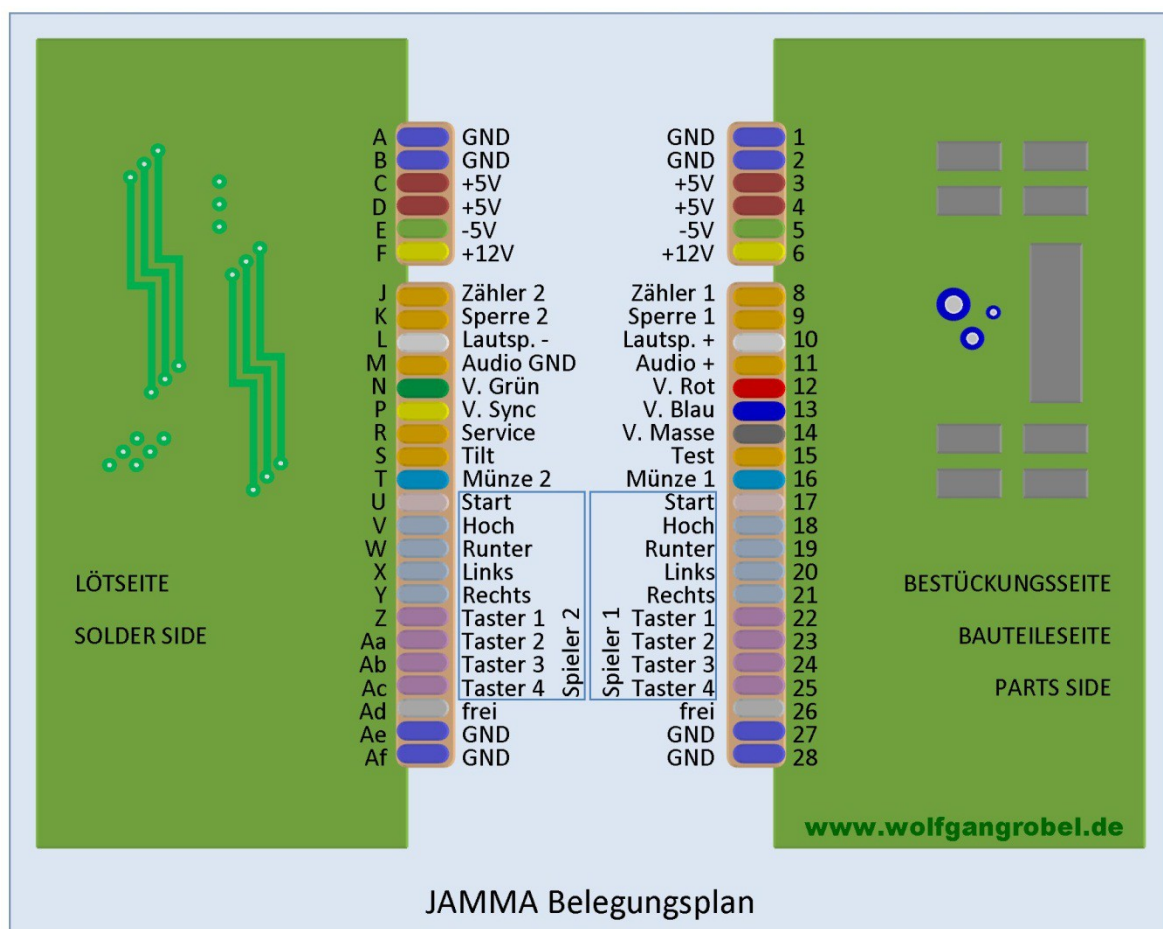


Mike McBike's unglaubliche unvollständige unverzichtbare IC-Liste

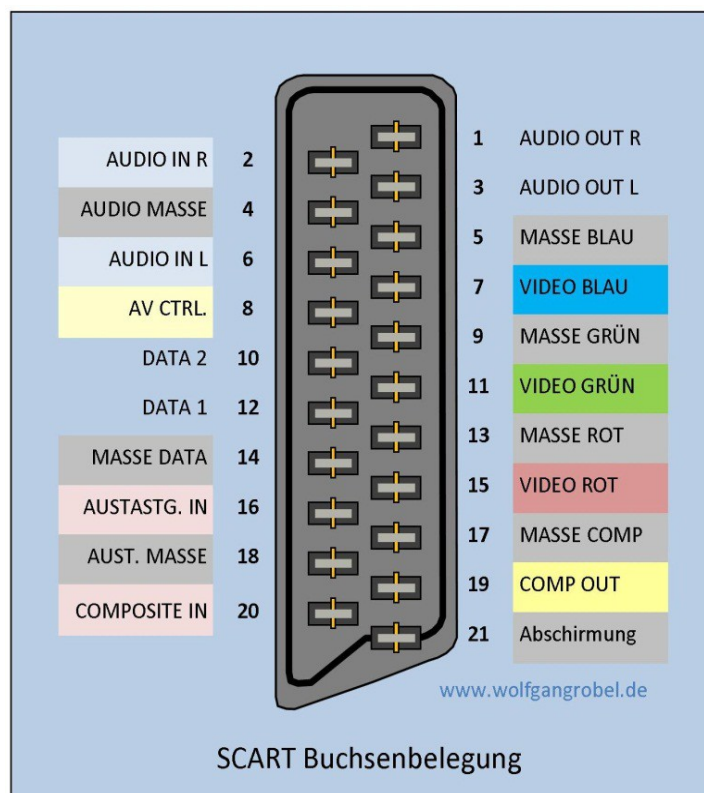




JAMMA - Connector



SCART - Connector





Automaten - TAB

Oberseite	Lötseite	Belegung
1		GND
	A	GND
2		GND
	B	GND
3		GND
	C	GND
4		+ 5V
	D	+ 5V
5		+12V
	E	+12V
6		- 5V
	F	- 5V
7		-12V
	H	-12V
8		Zähler 1
	J	Zähler + ???
9		Zähler 2
	K	Münzschalter 1
10		Münzschalter 2
	L	Tischumschaltung
11		1. Spieler Start
	M	1. Spieler Action-3
12		2. Spieler Start
	N	2. Spieler Action-3
13		1. Spieler Action-1
	P	1. Spieler Action-2
14		2. Spieler Action-1
	R	2. Spieler Action-2
15		1. Spieler rechts
	S	1. Spieler links
16		1. Spieler rauf
	T	1. Spieler runter
17		2. Spieler rechts
	U	2. Spieler links
18		2. Spieler rauf
	V	2. Spieler runter
19		Video Rot
	W	Video Grün
20		Video Blau
	X	Video Sync.
21		Video GND
	Y	Lautsprecher GND
22		Lautsprecher +
	Z	Testschalter

HELLOMAT

5. Anschlußbelegung TV - Adapter

10-poliger Stecker	12-poliger Stecker	16-poliger Stecker
1 + 5 V braun	1 Kredit weiß	1 1.Spieler links rot-schwarz
2 + 5 V braun	2 Münze ein weiß-rosa	2 2.Spieler links grau
3 - 5 V grau-rot	3 Video-Rot rot	3 1.Spieler rechts weiß-grau
4 + 12 V rot	4 Video-Grün grün	4 2.Spieler rechts weiß-rot
5 GND blau	5 Video-Blau blau	5 1.Spieler auf weiß-schwarz
6 GND blau	6 Video-Synchr. braun	6 2.Spieler auf gelb-braun
7 GND blau	7 + Lautspr. weiß-violett	7 1.Spieler ab braun-schwarz
8 GND-Video schwarz	8 1.Spieler-Taster 4 weiß-grün	8 2.Spieler ab grün-schwarz
9 GND-Lautspr. gelb-rot	9 Lautspr.2 weiß-blau	9 1.Spieler-Taster 1 gelb
10 Reset grau-blau	10 Lautspr.2 gelb-blau	10 2.Spieler-Taster 1 weiß-braun
	11 2.Spieler-Taster 4 gelb-weiß	11 1.Spieler-Taster 2 gelb-schwarz
	12 - -	12 2.Spieler-Taster 2 grün-rot
		13 1.Spieler-Taster 3 violett
		14 2.Spieler-Taster 3 rosa-rot
		15 1.Spieler Start rosa
		16 2.Spieler Start orange



HELLOMAT

"PULTANSCHLUS" Electronic Star

1	GND	= blau	0,5 ²
2	+ 12V	= rot	0,5 ²
3	1. Spieler	= rosa	0,25 ²
4	2. Spieler	= grau-blau	
5	1. Spieler auf	= weiß-schwarz	
6	1. Spieler ab	= braun-schwarz	
7	1. Spieler links	= rot-schwarz	
8	1. Spieler rechts	= weiß-grau	
9	1. Taste Spieler 1	= gelb	
10	2. Taste Spieler 1	= gelb-schwarz	
11	3. Taste Spieler 1	= violett	
12	4. Taste Spieler 1	= weiß-grün	
1	2. Spieler links	= grau	0,25 ²
2	2. Spieler rechts	= weiß-rot	
3	2. Spieler auf	= gelb-braun	
4	2. Spieler ab	= grün-schwarz	
5	1. Taste Spieler 2	= weiß-braun	
6	2. Taste Spieler 2	= grün-rot	
7	3. Taste Spieler 2	= rosa-rot	
8	4. Taste Spieler 2	= gelb-weiß	
9	SONDER 1	= weiß-blau	
10	SONDER 2	= gelb-blau	

16-pol Stecker

Pin	Funktion	Kabelfarbe
1●	1. Spieler – links	rot-schwarz
2	2. Spieler – links	grau
3	1. Spieler – rechts	weiß-grau
4	2. Spieler – rechts	weiß-rot
5	1. Spieler – auf	weiß-schwarz
6	2. Spieler – auf	gelb-braun
7	1. Spieler – ab	braun-schwarz
8	2. Spieler – ab	grün-schwarz
9	1. Spieler – Taste 1	gelb
10	2. Spieler – Taste 1	weiß-braun
11	1. Spieler – Taste 2	gelb-schwarz
12	2. Spieler – Taste 2	grün-rot
13	1. Spieler – Taste 3	violett
14	2. Spieler – Taste 3	rosa-rot
15	1. Spieler – Start	rosa
16	2. Spieler – Start	orange

Die HELLOMAT-Schnittstelle

10-pol Stecker

Pin	Funktion	Kabelfarbe
1●	+5V	braun
2	+5V	braun
3	-5V	grau-rot
4	+12V	rot
5	GND	blau
6	GND	blau
7	GND	blau
8	Video-GND	schwarz
9	Lautsprecher (-)	gelb-rot
10	NC	---

12-pol Stecker

Pin	Funktion	Kabelfarbe
1●	NC	---
2	Münze ein	weiß-rosa
3	Video-Rot	rot
4	Video-Grün	grün
5	Video-Blau	blau
6	Video-Sync	braun
7	Lautsprecher (+)	weiß-violett
8	1. Spieler – Taste 4	weiß-grün
9	NC	---
10	NC	---
11	2. Spieler – Taste 4	weiß-gelb
12	NC	----



Adp Gauselmann

adp Gauselmann ADAPTER-SYSTEM									
schwarz	GND	1	A	GND	schwarz				
schwarz	GND	2	B	GND	schwarz				
rot (dick)	+5 V	3	C	+5 V	rot (dick)				
rot (dick)	+5 V	4	D	+5 V	rot (dick)				
violett (dick)	+12 V	5	E	+12 V	violett (dick)				
grau-rot	-5 V	6	F	-5 V	grau-rot				
schwarz	GND	7	H	GND	schwarz				
		8	I						
		9	J						
gelb-rot	Lautspr.	10	L						
weiß-violett	Lautspr.	11	M	1. Sp. Taste 1	gelb				
grau-blau	Reset/Tilt	12	N	2. Sp. Taste 1	weiß-braun				
gelb-braun	2. Sp. oben	13	P	1. Sp. oben	weiß-schwarz				
	4. Sp. links	14	R	2. Sp. Taste 2					
weiß-rot	2. Sp. rechts	15	S	1. Sp. rechts	weiß-grau				
		16	T						
grün-schwarz	2. Sp. unten	17	U	1. Sp. unten	braun-schwarz				
		18	V						
grau	2. Sp. links	19	W	1. Sp. links	rot-schwarz				
		20	X						
orange	2. Sp. Start	21	Y	1. Sp. Start	rosa				
rot (dünn)	Video Rot	22	Z	1. Sp. Taste 2	gelb-schwarz				
grün	Video Grün	23	a	2. Sp. Taste 2	grün-rot				
blau	Video Blau	24	b	Münzzähler	weiß-blau				
braun	Video Sync	25	c	Münzeingang	weiß-rosa				
schwarz	GND	26	d	GND	schwarz				
schwarz	GND	27	e	GND	schwarz				
		28	f						
		29	h						
		30	i						

— = Kodierstift

Wir garantieren für dieses Adaptersystem immer die gleichen Drahtfarben und eine elektronisch geprüfte Durchkontaktierung der +5 V und der GND. Ansicht gegen die Stirnseite der Messerleiste.

video klein

Südstraße 42 - 4132 Kamp-Lintfort - Telefon (028 42) 66 00 Q 65 85

TV Ideal

TV IDEAL

Norm-Steckerbelegung adp TV-Standgerät

		B	A		
schwarz	GND	31	1	GND	
	GND	32	2	GND	
rot	+5V	33	3	+5V	
	+5V	34	4	+5V	
gelb	+12V	35	5	+12V	
	-5V	36	6	-5V	
violett	GND	37	7	GND	
rosa	pos. einstellb. Spannung	38	8	pos. einstellb. Spannung	
grün	neg. einstellb. Spannung	39	9	neg. einstellb. Spannung	
	Reservefunktion	40	10	Lautsprecher -	
	1 P Funktion I	41	11	Lautsprecher +	
	Zs. 9	42	12	Reset / Tilt	
	Steuerschalter 1 hoch	43	13	Zs. 14	
(Zs. 4) Steuerschalter 2 hoch		44	14	Zs. 7 2 P Funktion III	
Steuerschalter 1 rechts		45	15	Zs. 13	
(Zs. 3) Steuerschalter 2 rechts		46	16	Zs. 8 1 P Funktion III	
Steuerschalter 1 runter		47	17	Zs. 12	
(Zs. 2) Steuerschalter 2 runter		48	18	Zs. 6 2 P Funktion I	
Steuerschalter 1 links		49	19	Zs. 11	
(Zs. 1) Steuerschalter 2 links		50	20	Zs. 5 2 P Funktion II	
	1 Spieler	51	21	2 Spieler	
	1 P Funktion II	52	22	rot	
	Zs. 10	53	23	grün	
	Spielezähler	54	24	blau	
	Münzeingang	55	25	Sync.	
	GND	56	26	GND	
	GND	57	27	GND	
	GND	58	28	GND	
Lautsprecher 2 -		59	29	Lautsprecher 2 +	
3 Spieler		60	30	4 Spieler	

Steckerleiste

AMP-EDGE 530 655-9


LOGIK – TTL ÜBERSICHT – Teil 1

Typ	Pins	Anzahl	Beschreibung
00	14	4	2 Input NAND
01	14	4	2 Input NAND (OC=Open Collector)
02	14	4	2 Input NOR
03	14	4	2 Input NAND (OC) Andere Belegung als 7401
04	14	6	Inverter
05	14	6	Inverter (OC)
06	14	6	Inverter Buffer/Treiber (OC)
07	14	6	Buffer/Treiber (OC)
08	14	4	2 Input AND
09	14	4	2 Input AND (OC)
10	14	3	3 Input NAND
11	14	3	3 Input AND
12	14	3	3 Input NAND (OC)
13	14	2	4 Input NAND Schmitt-Trigger
14	14	6	Inverter Schmitt-Trigger
15	14	3	3 Input AND (OC)
16	14	6	Inverter Treiber (OC)
17	14	6	Treiber (OC)
18	14	2	4 Input NAND Schmitt-Trigger
19	14	6	Inverter Schmitt-Trigger
20	14	2	4 Input NAND
21	14	2	4 Input AND
22	14	2	4 Input NAND (OC)
24	14	4	2 Input NAND Schmitt-Trigger
25	14	2	4 Input NOR
26	14	4	2 Input NAND (OC)
27	14	3	3 Input NOR
28	14	4	2 Input NOR
30	14	1	8 Input NAND
31	16	-	Verzögerungs-Element (je 2 Non-Inverting, Inverting, 2 Input NAND)
32	14	4	2 Input OR
33	14	4	2 Input NOR (OC)
34	14	6	Treiber
35	14	6	Treiber (OC)
36	14	4	2 Input NOR
37	14	4	2 Input NAND
38	14	4	2 Input NAND (OC)
39	14	4	2 Input NAND Treiber (OC)
40	14	2	4 Input NAND
41	16	-	BCD -> Decimal Decoder (OC)
42	16	-	BCD -> Decimal Decoder



LOGIK – TTL ÜBERSICHT – Teil 2

Typ	Pins	Anzahl	Beschreibung
43	16	-	Excess-3 -> Decimal Decoder
44	16	-	Excess-3-Gray -> Decimal Decoder
45	16	-	BCD -> Decimal Decoder (OC)
46	16	-	BCD -> 7-Segment Decoder (OC)
47	16	-	BCD -> 7-Segment Decoder (OC)
48	16	-	BCD -> 7-Segment Decoder (OC)
49	14	-	BCD -> 7-Segment Decoder (OC)
50	14	-	Dual 2-Wide 2-Input AND-OR-INVERT (1 expandable)
51	14	2	AND-OR-INVERT
52	14	?	Expandable AND-OR
53	14	1	Expandable 4-Wide AND-OR-INVERT
54	14	1	3-2-2-3 Input AND-OR-INVERT
55	14	1	2-Wide 4-Input AND-OR-INVERT
72	14	1	And-Gated-JK-MS Flipflop with preset & clear
73	14	2	JK Flipflop with clear
74	14	2	D Flipflop with preset & clear
75	16	-	4-Bit Bistable Latch
76	16	2	JK Flipflop with preset & clear
78	14	2	JK Flipflop
86	14	4	2 Input XOR
90	14	-	Decade Counter
92	14	-	Divide By-Twelve Counter
93	14	-	4-Bit Binary Counter
95	14	-	4 Bit Parallel Access Shift Register
107	14	2	JK Flipflop with clear
112	16	2	JK Flipflop with preset & clear
121	14	-	Monostable Multivibrator With Schmitt-Trigger
122	14	-	Retriggerable Monostable Multivibrator
123	16	2	Retriggerable Monostable Multivibrator
125	14	4	Tri-State Buffer
126	14	4	Tri-State Buffer
132	14	4	2 Input NAND Schmitt-Trigger
133	16	1	13 Input NAND
136	14	4	2 Input XOR
137	16	1	3-to-8 line decoder / demultiplexer with address latches, low-active outputs
138	16	1	3-to-8 line decoder / demultiplexer , low-active outputs
139	16	2	2-to-4 line decoder / demultiplexer , low-active outputs
146	16	-	BCD -> Decimal Decoder (OC)
147	16	-	10-Line -> 4-Line BCD Priority Encoder
148	16	-	8-Line -> 3-Line Priority Encoder
151	16	1	8:1 Multiplexer



LOGIK – TTL ÜBERSICHT – Teil 3

Typ	Pins	Anzahl	Beschreibung
154	24	1	4-Line -> 16-Line Decoder/Demultiplexer
155	16	2	2-Line -> 4-Line Decoder/Demultiplexer
156	16	2	2-Line -> 4-Line Decoder/Demultiplexer (OC)
157	16	4	2:1 Multiplexer
158	16	4	2:1 Multiplexer , inverted outputs
160	16	-	Sync 4 Bit Binary Counter Async Reset
161	16	-	Sync 4 Bit Binary Counter Async Reset
162	16	-	Sync 4 Bit Binary Counter Async Reset
163	16	-	Sync 4 Bit Binary Counter Async Reset
164	14	-	8 Bit Serial Shift Register
165	16	-	8-Bit Parallel -> Serial (PISO) Shift Register0
166	16	-	4 bit shift register
173	16	4	D-Type Register
174	16	6	Hex-D-FF
175	16	4	Quad D-Flip-Flop
177	14	-	Presetable Binary Counter/Latch
190	16	-	Decimal Up/Down Counter
191	16	-	4-Bit Up/Down Binary Converter
192	16	-	Decimal Up/Down Counter mit Clear
193	16	-	4-Bit Up/Down Binary Counter mit Clear
194	16	-	4-Bit Bidirectional Universal Shift Register
221	16	2	Monostable Multivibrator with Reset
238	16	1	3-to-8 line decoder / demultiplexer , high-active outputs
239	16	2	2-to-4 line decoder / demultiplexer , high-active outputs
240	20	-	8-Bit Tri-State Buffer/Line Driver (invertierend)
241	20	-	8-Bit Tri-State Buffer/Line Driver
242	14	-	4-Bit Bus Transceiver (invertierend)
243	14	-	4-Bit Bus Transceiver (nicht invertierend)
244	20	-	8-Bit Tri-State Buffer/Line Driver (nicht invertierend) - Variante: 16244: 16-Bit-Version
245	20	-	8-Bit Bus Transceiver - Variante: 16T245: 16-Bit-Version
251	16	-	8-Bit Input Multiplexer; 3-State
259	16	-	8-Bit Adressable Latch
260	14	2	5 Input NOR
266	14	4	2 Input Exclusive NOR (OC)
273	20	-	8-fach D-FlipFlop mit Clear
283	16	-	4-Bit Volladdierer
288	16	-	256-bit PROM
299	20	-	8-Bit Universal Shift Register, Common IO-Pins, 3-State
366	16	6	Tri-State Inverting Buffer
367	16	6	Tri-State Buffer
373	20	-	8-Bit Transparent Latch
374	20	-	8-Bit Positiv Edge Triggered Register



LOGIK – TTL ÜBERSICHT – Teil 4

Typ	Pins	Anzahl	Beschreibung
393	14	2	4-Bit Binary Counter
541	20	-	8-Bit Tri-State Buffer/Line Driver
573	20	-	8-Bit Tri-State D-Type Latch
574	20	8	Tri-State Flipflop
590	16	-	8-Bit binary counter, 3-state output register
595	16	-	8-Bit Serial -> Parallel (SIPO) Shift Register
669	16	-	Synchronous 4 bit up/down counter



LOGIK – TTL – TEIL 1				
74xx00 / 01	74xx02	74xx03	74xx04 / 05 / 06	74xx07
4x NAND2	4x NOR2	4x NAND2 OC	6x INV	6x BUFFER
74xx08 / 09	74xx10	74xx11 / 12	74xx13	74xx14
4x AND	3x NAND3	3x AND3	2x NAND4 ST	6x INV ST
74xx20	74xx26	74xx30	74xx32	74xx42/43/44
2x NAND4	4x NAND2 OC	1x NAND8	4x OR2	BCD-Dezimal
74xx47	74xx73	74xx74	74xx86	74xx89
BCD-7-Segment	FF	FF	4x XOR2	RAM 16x4 OC



LOGIK – TTL – TEIL 2				
74xx90	74xx92	74xx93	74xx122	74xx123
4-Bit Zähler 0..9	4-Bit Zähler 0..11	4-Bit Zähler 0..15	Single Monoflop	Dual Monoflop
74xx125	74xx132	74xx138	74xx139	74xx157
4x tristate buffer	4x NAND2 ST	3-to-8 line decoder	2x 2-to-4 decoder	4x Multiplexer 2:1
74xx160 / 161 / 162 / 163	74xx166	74xx173	74xx174	74xx175
4-bit Binärzähler	4Bit Shift Reg.	4 Bit D-Register	Hex D-Flip-Flop	Quad D-FF
74xx181	74xx189	74xx190 / 191	74xx192 / 193	74xx194
4-Bit ALU	RAM 16x4 TS	Up/down Counter	presetable counter	4 Bit unid. Shift Reg.



LOGIK – TTL – TEIL 3				
74xx240	74xx241	74xx244	74xx245	
8x Bustreiber	8x Bustreiber	8x Bustreiber	8x Bustreiber Bidir.	
74xx253	74xx257	74xx258	74xx273	
4 Input-Mux	Quad 2-Data-Mux	Quad 2-Data-Mux	8x D-FF m. Clear	
74xx283	74xx299	74xx365	74xx366	74xx367
4 Bit Addierer	8-Bit-Shift Register	6x tristate buffer	6x tristate inverter	6x tristate buffer



LOGIK – TTL – TEIL 4				
74xx368	74xx373	74xx374	74xx377	74xx378
6x tristate inverter	8 Bit transp. Latch	8 Bit transp. Latch	8x FF	6x FF
74xx379	74xx669			8216
4x FF	4 Bit u/d Counter			Bus-Driver



LOGIK – CMOS – ÜBERSICHT TEIL 1

4000	Dual 3-input NOR gate + 1 Inverter
4001	Quad 2-input NOR gate
4002	Dual 4-input NOR gate
4006	18 stage Shift register
4007	Dual Complementary Pair Plus Inverter
4008	4 bit adder
4009	Hex inverting buffer
4010	Hex non-inverting buffer
4011	Buffered Quad 2-Input NAND gate
4012	Dual 4-input NAND gate
4013	Dual D-type flip-flop
4014	8-stage shift register
4015	Dual 4-stage shift register
4016	Quad bilateral switch
4017	Divide-by-10 counter (5-stage Johnson counter)
4018	Presettable divide-by-n counter
4019	Quad AND-OR Select Gate
4020	14-stage binary counter
4021	8-bit static shift register
4022	Divide-by-8 counter (4-stage Johnson counter)
4023	Triple 3-input NAND gate
4024	7-Stage Binary Ripple Counter
4025	Triple 3-input NOR gate
4026	BCD counter with decoded 7-segment output
4027	Dual JK flip-flop
4028	BCD to decimal (1-of-10) decoder
4029	Presettable up/down counter, binary or BCD-decade
4030	Quad XOR gate (replaced by 4070)
4031	64-Bit Static Shift Register
4032	Triple serial adder
4033	BCD counter + 7-segment decoder w/ripple blank
4034	8-stage bidirectional parallel or serial input/parallel output
4035	4-stage parallel-in/parallel-out (PIPO) with J-K input and true/complement output
4038	Triple serial adder
4040	12-stage binary ripple counter
4041	Quad true/complement buffer
4042	Quad D-type latch
4043	Quad NOR R/S latch
4044	Quad NAND R/S latch (tristate output)
4045	21-Stage Counter
4046	PLL with VCO
4047	Monostable/Astable Multivibrator
4048	Multifunctional expandable 8-input (tristate output)
4049	Hex inverter/buffer (NOT gate)
4050	Hex buffer/converter (non-inverting)
4051	Analogue multiplexer/demultiplexer (1-of-8 switch)
4052	Analogue multiplexer/demultiplexer (Dual 1-of-4 switch)
4053	Analogue multiplexer/demultiplexer (Triple 1-of-2 switch)
4054	7-segment decoder/LCD driver
4055	BCD-to-7-segment decoder/driver with "display-frequency" output
4056	BCD-to-7-segment decoder/driver with strobed latch function
4059	Programmable divide-by-N counter
4060	14-stage binary ripple counter and oscillator
4062	Logic dual 3 majority gate
4063	4-bit Digital comparator
4066	Quad Analog switch (Low "ON" Resistance)
4067	16-channel analogue multiplexer/demultiplexer (1-of-16 switch)
4068	8-input NAND gate
4069	Hex inverter
4070	Quad XOR gate
4071	Quad 2-input OR gate
4072	Dual 4-input OR gate
4073	Triple 3-input AND gate
4075	Triple 3-input OR gate
4076	Quad D-type register with tristate outputs
4077	Quad 2-input XNOR gate
4078	8-input NOR gate
4081	Quad 2-input AND gate
4082	Dual 4-input AND gate
4085	Dual 2-wide, 2-input AND/OR invert (AOI)
4086	Expandable 4-wide, 2-input AND/OR invert (AOI)
4089	Binary rate multiplier
4093	Quad 2-input Schmitt trigger NAND gate



LOGIK – CMOS – ÜBERSICHT TEIL 2

4094	8-stage shift-and-store bus
4095	Gated "J-K" (non-inverting)
4096	Gated "J-K" (inverting and non-inverting)
4097	Differential 8-channel analog multiplexer/demultiplexer
4098	Dual one-shot monostable
4099	8-bit addressable latch
4104	Quad Low-to-High Voltage Translator with tristate outputs
4502	Hex inverting buffer (tristate)
4503	Hex non-inverting buffer with tristate outputs
4504	Hex voltage level shifter for TTL-to-CMOS or CMOS-to-CMOS operation
4508	Dual 4-bit latch with tristate outputs
4510	Presetable 4-bit BCD up/down counter
4511	BCD to 7-segment latch/decoder/driver
4512	8-input multiplexer (data selector) with tristate output
4513	BCD to 7-segment latch/decoder/driver (4511 plus ripple blanking)
4514	1-of-16 decoder/demultiplexer active HIGH output
4515	1-of-16 decoder/demultiplexer active LOW output
4516	Presetable 4-bit binary up/down counter
4517	Dual 64-Bit Static Shift Register
4518	Dual BCD up counter
4519	Quad 2-input multiplexer (data selector)
4520	Dual 4-bit binary up counter
4521	24-stage frequency divider
4522	Programmable BCD divide-by-N counter
4526	Programmable 4-bit binary down counter
4527	BCD rate multiplier
4528	Dual Retriggerable Monostable Multivibrator with Reset
4529	Dual 4-channel analog
4532	8-bit priority encoder
4536	Programmable Timer
4538	Dual Retriggerable Precision Monostable Multivibrator
4539	Dual 4-input multiplexer
4541	Programmable Timer
4543	BCD to 7-Segment Latch/Decoder/Driver with Phase Input
4553	3-digit BCD counter
4555	Dual 1-of-4 decoder/demultiplexer active HIGH output
4556	Dual 1-of-4 decoder/demultiplexer active LOW output
4557	1-to-64 Bit Variable Length Shift Register
4560	NBCD adder
4566	Industrial time-base generator
4572	Hex gate : quad NOT, single NAND, single NOR
4584	Hex schmitt trigger
4585	4-bit Digital comparator
4724	8-bit addressable latch
4750	Frequency synthesizer
4751	Universal divider
4794	8-Stage Shift-and-Store Register LED Driver
4894	12-Stage Shift-and-Store Register LED Driver
4938	Dual Retriggerable Precision Monostable Multivibrator with Reset
4952	8-channel analog multiplexer/demultiplexer
40098	3-state hex inverting buffer
40100	32-bit left/right Shift Register
40101	9-bit Parity Generator/Checker
40102	Presetable 2-decade BCD down counter
40103	Presetable 8-bit binary down counter
40104	4 bit bidirectional Parallel-in/Parallel-out PIPO Shift Register (tristate)
40105	4-bit x 16 word Register
40106	Hex Inverting Schmitt-Trigger-(NOT gates)
40107	dual 2-input NAND buffer/driver
40108	4x4-bit (tristate) synchronous triple-port register file
40109	Quad level shifter
40110	Up/Down Counter-Latch-Decoder-Driver
40116	8-bit bidirectional CMOS-to-TTL level converter
40117	Programmable dual 4-bit terminator
40147	10-line to 4-line [BCD] priority encoder
40160	Decade counter/asynchronous clear
40161	Binary counter/asynchronous clear
40162	4-bit synchronous decade counter with load, reset, and ripple carry output
40163	4-bit synchronous binary counter with load, reset, and ripple carry output
40174	Hex D-type flip-flop with reset; positive-edge trigger
40175	Quad D-type flip-flop with reset; positive-edge trigger
40181	4-bit 16-function arithmetic logic unit (ALU)
40192	Presetable 4-bit BCD up/down counter



LOGIK – CMOS – ÜBERSICHT TEIL 3

40193	Presettable 4-bit binary up/down counter
40194	4-bit universal bidirectional with asynchronous master reset
40195	4-bit universal shift register
40208	4 × 4-bit (tristate) synchronous triple-port register file
40240	Buffer/Line driver; Inverting (tristate)
40244	Buffer/Line Driver; Non-Inverting (tristate)
40245	Octuple bus transceiver; (tristate) outputs,
40257	Quad 2-Line-to-1-Line Data Selector/Multiplexer (tristate)
40373	Octal D-Type Transparent latch (tristate)
40374	Octal D-type flip-flop; positive-edge trigger (tristate)

LOGIK - CMOS

4000	4001	4002
CD4000BMS	CD4001BMS	CD4002BMS
4025	4066	
CD4025BMS	Quad Analog Switch	



SPEICHER – EPROM 8 BIT – Teil 1

1702	2704	2708																														
256 x 8	512 x 8 (+5 / -5 / 12V)	EPROM 1k x 8 (+5 / -5 / 12V)																														
	*2708 = A ₉ 2704 = V _{SS}	*2708 = A ₉ 2704 = V _{SS}																														
2716	TMS2516	TMS2716																														
EPROM 2k x 8	EPROM 2k x 8	EPROM 2k x 8 (+5 / -5 / 12V)																														
	<table><tr><th colspan="2">PIN NOMENCLATURE</th></tr><tr><td>A(N)</td><td>Address inputs</td></tr><tr><td>CS</td><td>Chip Select</td></tr><tr><td>PD/PGM</td><td>Power Down/Program</td></tr><tr><td>Q(N)</td><td>Input/Output</td></tr><tr><td>Vcc</td><td>+5 V Power Supply</td></tr><tr><td>Vpp</td><td>+25 V Power Supply</td></tr><tr><td>Vss</td><td>0 V Ground</td></tr></table>	PIN NOMENCLATURE		A(N)	Address inputs	CS	Chip Select	PD/PGM	Power Down/Program	Q(N)	Input/Output	Vcc	+5 V Power Supply	Vpp	+25 V Power Supply	Vss	0 V Ground															
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TMS2532	2732	TMS2564																														
EPROM 4k x 8	EPROM 4k x 8	EPROM 8k x 8																														
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Vcc	+5 V Power Supply																															
Vpp	+25 V Power Supply																															
Vss	0 V Ground																															



SPEICHER – EPROM 8 BIT – Teil 2

27x64	27x128	27x256
EPROM 8k x 8	EPROM 16k x 8	EPROM 32k x 8
Pinout for 27x64 EPROM (M2764A): - V_{PP} 1, V_{CC} 28 - A₁₂ 2, $\bar{P}$ 27 - A₇ 3, NC 26 - A₆ 4, A₈ 25 - A₅ 5, A₉ 24 - A₄ 6, A₁₁ 23 - A₃ 7, $\bar{G}$ 22 - A₂ 8, A₁₀ 21 - A₁ 9, $\bar{E}$ 20 - A₀ 10, Q₇ 19 - Q₀ 11, Q₆ 18 - Q₁ 12, Q₅ 17 - Q₂ 13, Q₄ 16 - V_{SS} 14, Q₃ 15	Pinout for 27x128 EPROM: - V_{PP} 1, V_{CC} 28 - A₁₂ 2, $\bar{P}$ 27 - A₇ 3, A₁₃ 26 - A₆ 4, A₈ 25 - A₅ 5, A₉ 24 - A₄ 6, A₁₁ 23 - A₃ 7, $\bar{G}$ 22 - A₂ 8, A₁₀ 21 - A₁ 9, $\bar{E}$ 20 - A₀ 10, Q₇ 19 - Q₀ 11, Q₆ 18 - Q₁ 12, Q₅ 17 - Q₂ 13, Q₄ 16 - V_{SS} 14, Q₃ 15	Pinout for 27x256 EPROM: - V_{PP} 1, V_{CC} 28 - A₁₂ 2, A₁₄ 27 - A₇ 3, A₁₃ 26 - A₆ 4, A₈ 25 - A₅ 5, A₉ 24 - A₄ 6, A₁₁ 23 - A₃ 7, $\bar{G}$ 22 - A₂ 8, A₁₀ 21 - A₁ 9, $\bar{E}$ 20 - A₀ 10, Q₇ 19 - Q₀ 11, Q₆ 18 - Q₁ 12, Q₅ 17 - Q₂ 13, Q₄ 16 - V_{SS} 14, Q₃ 15
27x512	27x010	27C301 / 27C1000
EPROM 64k x 8	EPROM 128k x 8	EPROM 128k x 8 non JEDEC
Pinout for 27x512 EPROM (M27C512): - A₁₅ 1, V_{CC} 28 - A₁₂ 2, A₁₄ 27 - A₇ 3, A₁₃ 26 - A₆ 4, A₈ 25 - A₅ 5, A₉ 24 - A₄ 6, A₁₁ 23 - A₃ 7, $\bar{G}$V_{PP} 22 - A₂ 8, A₁₀ 21 - A₁ 9, $\bar{E}$ 20 - A₀ 10, Q₇ 19 - Q₀ 11, Q₆ 18 - Q₁ 12, Q₅ 17 - Q₂ 13, Q₄ 16 - V_{SS} 14, Q₃ 15	Pinout for 27x010 EPROM (M27C1001): - V_{PP} 1, V_{CC} 32 - A₁₆ 2, $\bar{P}$ 31 - A₁₅ 3, NC 30 - A₁₂ 4, A₁₄ 29 - A₇ 5, A₁₃ 28 - A₆ 6, A₈ 27 - A₅ 7, A₉ 26 - A₄ 8, A₁₁ 25 - A₃ 9, $\bar{G}$ 24 - A₂ 10, A₁₀ 23 - A₁ 11, $\bar{E}$ 22 - A₀ 12, Q₇ 21 - Q₀ 13, Q₆ 20 - Q₁ 14, Q₅ 19 - Q₂ 15, Q₄ 18 - V_{SS} 16, Q₃ 17	Pinout for 27C301 / 27C1000 EPROM: - V_{PP} 1, V_{CC} 32 $\bar{OE}$ 2, PGM 31 - A₁₅ 3, NC 30 - A₁₂ 4, A₁₄ 29 - A₇ 5, A₁₃ 28 - A₆ 6, A₈ 27 - A₅ 7, A₉ 26 - A₄ 8, A₁₁ 25 - A₃ 9, A₁₆ 24 - A₂ 10, A₁₀ 23 - A₁ 11, CE 22 - A₀ 12, Q₇ 21 - Q₀ 13, Q₆ 20 - Q₁ 14, Q₅ 19 - Q₂ 15, Q₄ 18 - GND 16, Q₃ 17
27x020 / 27C2001	27C040 / 27C4001	27C080
EPROM 256k x 8	EPROM 512k x 8	EPROM 1M x 8
Pinout for 27x020 / 27C2001 EPROM (M27C2001): - V_{PP} 1, V_{CC} 32 - A₁₆ 2, $\bar{P}$ 31 - A₁₅ 3, A₁₇ 30 - A₁₂ 4, A₁₄ 29 - A₇ 5, A₁₃ 28 - A₆ 6, A₈ 27 - A₅ 7, A₉ 26 - A₄ 8, A₁₁ 25 - A₃ 9, $\bar{G}$ 24 - A₂ 10, A₁₀ 23 - A₁ 11, $\bar{E}$ 22 - A₀ 12, Q₇ 21 - Q₀ 13, Q₆ 20 - Q₁ 14, Q₅ 19 - Q₂ 15, Q₄ 18 - V_{SS} 16, Q₃ 17	Pinout for 27C040 / 27C4001 EPROM: - V_{PP} 1, V_{CC} 32 - A₁₆ 2, A₁₈ 31 - A₁₅ 3, A₁₇ 30 - A₁₂ 4, A₁₄ 29 - A₇ 5, A₁₃ 28 - A₆ 6, A₈ 27 - A₅ 7, A₉ 26 - A₄ 8, A₁₁ 25 - A₃ 9, $\bar{OE}$ 24 - A₂ 10, A₁₀ 23 - A₁ 11, CE/PGM 22 - A₀ 12, Q₇ 21 - Q₀ 13, Q₆ 20 - Q₁ 14, Q₅ 19 - Q₂ 15, Q₄ 18 - GND 16, Q₃ 17	Pinout for 27C080 EPROM: - A₁₉ 1, V_{CC} 32 - A₁₆ 2, A₁₈ 31 - A₁₅ 3, A₁₇ 30 - A₁₂ 4, A₁₄ 29 - A₇ 5, A₁₃ 28 - A₆ 6, A₈ 27 - A₅ 7, A₉ 26 - A₄ 8, A₁₁ 25 - A₃ 9, $\bar{OE}$/V_{PP} 24 - A₂ 10, A₁₀ 23 - A₁ 11, CE 22 - A₀ 12, Q₇ 21 - Q₀ 13, Q₆ 20 - Q₁ 14, Q₅ 19 - Q₂ 15, Q₄ 18 - GND 16, Q₃ 17



SPEICHER – EPROM 16 BIT JEDEC

27C1024	27C2048 / 27C202	27C4096 / 27C4002 / 27C240 / 27C402
64k x 16	128k x 16	256k x 16

SPEICHER – FLASH PROM 8 BIT

29F010	29F020/002	29F040
Flash 128k x 8	Flash 256k x 8	Flash 512k x 8 Nicht EPROM kompatibel!


SPEICHER – EPROM 16 BIT MASK-PROM kompatibel

MX27C1100	MX27C2100	MX27C4100 / HN27C4000
64k x 16 / 128k x 8	128k x 16 / 256k x 8	256k x 16 / 512k x 8
MX27C1100 NC 1 40 A8 A7 2 39 A9 A6 3 38 A10 A5 4 37 A11 A4 5 36 A12 A3 6 35 A13 A2 7 34 A14 A1 8 33 A15 A0 9 32 NC CE 10 31 BYTE/VPP GND 11 30 GND OE 12 29 Q15/A-1 Q0 13 28 Q7 Q8 14 27 Q14 Q1 15 26 Q6 Q9 16 25 Q13 Q2 17 24 Q5 Q10 18 23 Q12 Q3 19 22 Q4 Q11 20 21 VCC	MX27C2100 NC 1 40 A8 A7 2 39 A9 A6 3 38 A10 A5 4 37 A11 A4 5 36 A12 A3 6 35 A13 A2 7 34 A14 A1 8 33 A15 A0 9 32 A16 CE 10 31 BYTE/VPP GND 11 30 GND OE 12 29 Q15/A-1 Q0 13 28 Q7 Q8 14 27 Q14 Q1 15 26 Q6 Q9 16 25 Q13 Q2 17 24 Q5 Q10 18 23 Q12 Q3 19 22 Q4 Q11 20 21 VCC	MX27C4100 A17 1 40 A8 A7 2 39 A9 A6 3 38 A10 A5 4 37 A11 A4 5 36 A12 A3 6 35 A13 A2 7 34 A14 A1 8 33 A15 A0 9 32 A16 CE 10 31 BYTE/VPP GND 11 30 GND OE 12 29 Q15/A-1 Q0 13 28 Q7 Q8 14 27 Q14 Q1 15 26 Q6 Q9 16 25 Q13 Q2 17 24 Q5 Q10 18 23 Q12 Q3 19 22 Q4 Q11 20 21 VCC
27C800	27C160	27C322
512k x 16 / 1M x 8	1M x 16 / 2M x 8	2M x 16
M27C800 A18 1 42 NC A17 2 41 A8 A7 3 40 A9 A6 4 39 A10 A5 5 38 A11 A4 6 37 A12 A3 7 36 A13 A2 8 35 A14 A1 9 34 A15 A0 10 33 A16 E 11 32 BYTEVpp VSS 12 31 VSS G 13 30 Q15A-1 Q0 14 29 Q7 Q8 15 28 Q14 Q1 16 27 Q6 Q9 17 26 Q13 Q2 18 25 Q5 Q10 19 24 Q12 Q3 20 23 Q4 Q11 21 22 VCC	M27C160 A18 1 42 A19 A17 2 41 A8 A7 3 40 A9 A6 4 39 A10 A5 5 38 A11 A4 6 37 A12 A3 7 36 A13 A2 8 35 A14 A1 9 34 A15 A0 10 33 A16 E 11 32 BYTEVpp VSS 12 31 VSS G 13 30 Q15A-1 Q0 14 29 Q7 Q8 15 28 Q14 Q1 16 27 Q6 Q9 17 26 Q13 Q2 18 25 Q5 Q10 19 24 Q12 Q3 20 23 Q4 Q11 21 22 VCC	M27C322 A18 1 42 A19 A17 2 41 A8 A7 3 40 A9 A6 4 39 A10 A5 5 38 A11 A4 6 37 A12 A3 7 36 A13 A2 8 35 A14 A1 9 34 A15 A0 10 33 A16 E 11 32 A20 VSS 12 31 VSS GVpp 13 30 Q15 Q0 14 29 Q7 Q8 15 28 Q14 Q1 16 27 Q6 Q9 17 26 Q13 Q2 18 25 Q5 Q10 19 24 Q12 Q3 20 23 Q4 Q11 21 22 VCC



SPEICHER – MASK PROM

MB831000		MB832000		MB834000	
128k x 8		256k x 8		512k x 8	
TC538200		TC5316200		TC5332202	
512k x 16 / 1M x 8		1M x 16 / 2M x 8		2M x 16 / 4M x 8	
HY23C64100 64 Mbit MPR0M					
4M X 1					

The diagram illustrates the pin assignments for various EPROM devices. The table below provides the pin numbers and their functions for each device.

Pin	27C080	27C040	27C020	27C010	27C000
1	1M	512k	256k	128k	128k
2	000FFF	000FFF	000FFF	000FFF	000FFF
3	12.75V	12.75V	12.75V	12.75V	12.75V
4	000FFF	000FFF	000FFF	000FFF	000FFF
5	12.75V	12.75V	12.75V	12.75V	12.75V
6	000FFF	000FFF	000FFF	000FFF	000FFF
7	12.75V	12.75V	12.75V	12.75V	12.75V
8	000FFF	000FFF	000FFF	000FFF	000FFF
9	12.75V	12.75V	12.75V	12.75V	12.75V
10	000FFF	000FFF	000FFF	000FFF	000FFF
11	12.75V	12.75V	12.75V	12.75V	12.75V
12	000FFF	000FFF	000FFF	000FFF	000FFF
13	12.75V	12.75V	12.75V	12.75V	12.75V
14	000FFF	000FFF	000FFF	000FFF	000FFF
15	12.75V	12.75V	12.75V	12.75V	12.75V
16	000FFF	000FFF	000FFF	000FFF	000FFF
17	12.75V	12.75V	12.75V	12.75V	12.75V
18	000FFF	000FFF	000FFF	000FFF	000FFF
19	12.75V	12.75V	12.75V	12.75V	12.75V
20	000FFF	000FFF	000FFF	000FFF	000FFF
21	12.75V	12.75V	12.75V	12.75V	12.75V
22	000FFF	000FFF	000FFF	000FFF	000FFF
23	12.75V	12.75V	12.75V	12.75V	12.75V
24	000FFF	000FFF	000FFF	000FFF	000FFF
25	12.75V	12.75V	12.75V	12.75V	12.75V
26	000FFF	000FFF	000FFF	000FFF	000FFF
27	12.75V	12.75V	12.75V	12.75V	12.75V
28	000FFF	000FFF	000FFF	000FFF	000FFF
29	12.75V	12.75V	12.75V	12.75V	12.75V
30	000FFF	000FFF	000FFF	000FFF	000FFF
31	12.75V	12.75V	12.75V	12.75V	12.75V
32	000FFF	000FFF	000FFF	000FFF	000FFF

09.08.23

v. 1.0 EPROM PINBELEGUNGSÜBERSICHT 16 Bit Typen 28.07.2014


SPEICHER – Statisches RAM (SRAM) – Teil 1

2114	UM6114	6116
1024 x 4 SRAM	1k x 4 SRAM	2k x 8 SRAM
6164	61256	61512
8k x 8 SRAM	32k x 8 SRAM	64k x 8 SRAM
93410	NTE2102	
256 x 1 SRAM	1k x 1 SRAM	


SPEICHER – Statisches RAM (SRAM) – Teil 2

AM93422	μPD5101	N82S09
Bipolar SRAM 256 x 4	SRAM 256 x 4	64 x 9 SRAM
2147	2148	2149
4k x 1 SRAM	4k x 1 SRAM Power Down	1k x 4 SRAM

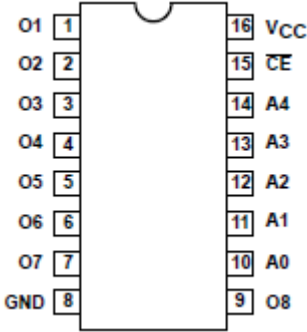
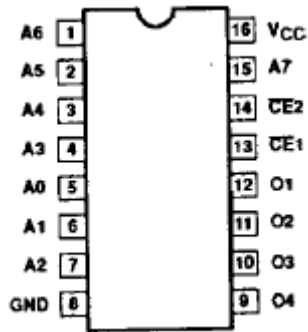
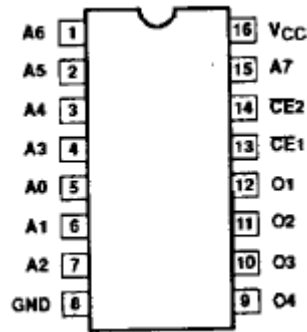
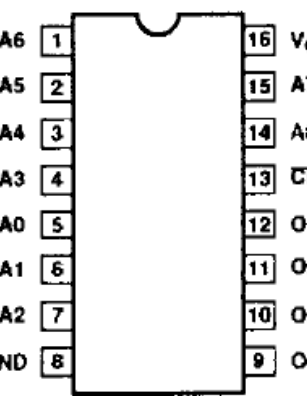
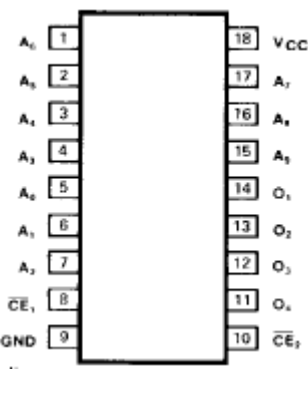
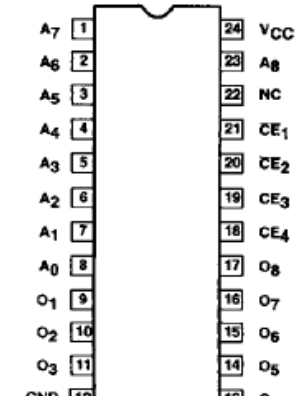
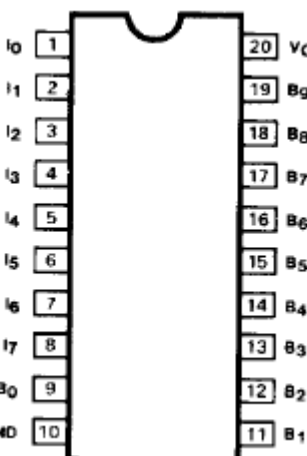
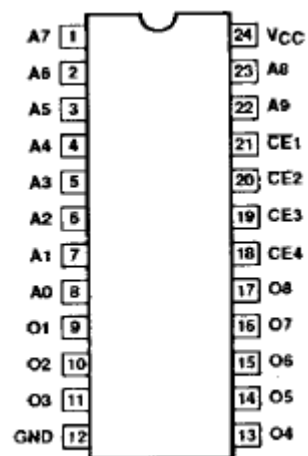
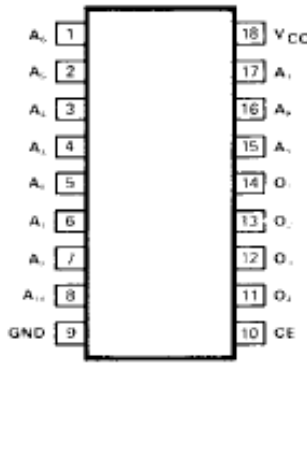


SPEICHER – Dynamisches RAM (DRAM)

MB8116 TMS4116	MB8118	4164
16kx1 DRAM Triple supply	16kx1 DRAM Single supply	64kx1 DRAM
μPD41256	M5M4464AL	
256kx1 DRAM	64kx4 DRAM	
	NC: NO CONNECTION NP: NO PIN	
TMS4060	61464	
4k x 1 DRAM	64k x 4 DRAM	
Vcc + 5V power supply Vss 0V ground Vdd +12V power supply Vbb - 5V power supply		



SPEICHER – PROM BIPOLAR – Teil 1

N82S123 / MMI 6331/ IM5610	N82S126 / 7610	N82S129 / MMI 6301 / MMI 63s141/ IM5623
32 x 8 TS	256 x 4 OC	256 x 4 TS
		
N82S131 / AM27S13 / 93466	N82S137 / 63S441 / HM1-7643-5	N82S141 / MMI-6341-1J
512 x 4 TS	1024 x 4 TS	512 x 8 TS
		
N82s153 / PLS153	N82S181 / HM1-7631-5	N82S185 / HM1-7685-5
PLA 80x42x10	1024 x 8 TS	2048 x 4 TS
		



SPEICHER – PROM BIPOLAR – Teil 2

N82S191 / AM27S191	N82S195 / HM1-76165-5	N82S100
2048 x 8	4096 x 4	PLA 16x48x8

8080				8085			
INTEL 8 Bit CPU				8 Bit CPU			
(GND)	A ₁₀	1	40	X ₁	1	40	V _{CC}
	V _{SS}	2	39	X ₂	2	39	Hold
	D ₄	3	38	Reset Out	3	38	HLDA
	D ₅	4	37	SOD	4	37	CLK
	D ₆	5	36	SID	5	36	RESET IN
	D ₇	6	35	Trap	6	35	Ready
	D ₃	7	34	RST 7.5	7	34	IO/ M
	D ₂	8	33	RST 6.5	8	33	S ₁
	D ₁	9	32	RST 5.5	9	32	RD
(-5V)	D ₀	10	31	INTR	10	31	WR
	V _{BB}	11	30	INTA	11	30	ALE
	Reset	12	29	AD ₀	12	29	S ₀
	Hold	13	28	AD ₁	13	28	A ₁₅
	INT	14	27	AD ₂	14	27	A ₁₄
	Phase ₂	15	26	AD ₃	15	26	A ₁₃
	INTE	16	25	AD ₄	16	25	A ₁₂
	DBIN	17	24	AD ₅	17	24	A ₁₁
	WRI	18	23	AD ₆	18	23	A ₁₀
	Sync	19	22	AD ₇	19	22	A ₉
(+5V)	V _{CC}	20	21	V _{SS}	20	21	A ₈

Z80				8088			
ZILOG 8 Bit CPU				Intel 8/16-Bit CPU			
A ₁₁	1	40	A ₁₀	GND	1	40	V _{CC}
A ₁₂	2	39	A ₉	A14	2	39	A15
A ₁₃	3	38	A ₈	A13	3	38	A16/S3
A ₁₄	4	37	A ₇	A12	4	37	A17/S4
A ₁₅	5	36	A ₆	A11	5	36	A18/S5
CLK	6	35	A ₅	A10	6	35	A19/S6
D ₄	7	34	A ₄	A9	7	34	SS0 (HIGH)
D ₃	8	33	A ₃	A8	8	33	MN/MX
D ₅	9	32	A ₂	AD7	9	32	RD
D ₆	10	31	A ₁	AD6	10	31	HOLD (RQ/GT0)
+5V	11	30	A ₀	AD5	11	30	HLDA (RQ/GT1)
D ₂	12	29	GND	AD4	12	29	WR (LOCK)
D ₇	13	28	RFSH	AD3	13	28	IO/ M (S2)
D ₀	14	27	M1	AD2	14	27	DT/R (S1)
D ₁	15	26	Reset	AD1	15	26	DEN (S0)
INT	16	25	BUSREQ	AD0	16	25	ALE (QS0)
NMI	17	24	Wait	NMI	17	24	INTA (QS1)
Halt	18	23	BUSACK	INTR	18	23	TEST
MREQ	19	22	WR	CLK	19	22	READY
IORQ	20	21	RD	GND	20	21	RESET



PROZESSOREN 8 BIT – Teil 2

6502	6510
MOS 8 Bit CPU	MOS 8 Bit CPU
Pin configuration for 6502 MOS 8 Bit CPU: - Pin 1: V_{SS} - Pin 2: RDY - Pin 3: Phase₁ (Out) - Pin 4: IRQ - Pin 5: Not connected - Pin 6: NMI - Pin 7: Sync - Pin 8: V_{CC} - Pin 9: AB₀ - Pin 10: AB₁ - Pin 11: AB₂ - Pin 12: AB₃ - Pin 13: AB₄ - Pin 14: AB₅ - Pin 15: AB₆ - Pin 16: AB₇ - Pin 17: AB₈ - Pin 18: AB₉ - Pin 19: AB₁₀ - Pin 20: AB₁₁ - Pin 21: V_{SS} - Pin 22: AB₁₂ - Pin 23: AB₁₃ - Pin 24: AB₁₄ - Pin 25: AB₁₅ - Pin 26: DB₇ - Pin 27: DB₆ - Pin 28: DB₅ - Pin 29: DB₄ - Pin 30: DB₃ - Pin 31: DB₂ - Pin 32: DB₁ - Pin 33: DB₀ - Pin 34: R/W - Pin 35: Not connected - Pin 36: Not connected - Pin 37: Phase₀ - Pin 38: SO - Pin 39: Phase₂ - Pin 40: RES	6510 Pin configuration for 6510 MOS 8 Bit CPU: - Pin 1: $\phi 0$ in - Pin 2: RDY - Pin 3: IRQ - Pin 4: NMI - Pin 5: AEC - Pin 6: UCC - Pin 7: A₀ - Pin 8: A₁ - Pin 9: A₂ - Pin 10: A₃ - Pin 11: A₄ - Pin 12: A₅ - Pin 13: A₆ - Pin 14: A₇ - Pin 15: A₈ - Pin 16: A₉ - Pin 17: A₁₀ - Pin 18: A₁₁ - Pin 19: A₁₂ - Pin 20: A₁₃ - Pin 21: GND - Pin 22: A₁₄ - Pin 23: A₁₅ - Pin 24: P₅ - Pin 25: P₄ - Pin 26: P₃ - Pin 27: P₂ - Pin 28: P₁ - Pin 29: P₀ - Pin 30: D₇ - Pin 31: D₆ - Pin 32: D₅ - Pin 33: D₄ - Pin 34: D₃ - Pin 35: D₂ - Pin 36: D₁ - Pin 37: D₀ - Pin 38: R/W - Pin 39: $\phi 2$ out - Pin 40: RES
6522	6532
VIA – Versatile Interface Adapter	RIOT – RAM-IO-Timer
Pin configuration for 6522 VIA – Versatile Interface Adapter: - Pin 1: V_{SS} - Pin 2: PA₀ - Pin 3: PA₁ - Pin 4: PA₂ - Pin 5: PA₃ - Pin 6: PA₄ - Pin 7: PA₅ - Pin 8: PA₆ - Pin 9: PA₇ - Pin 10: PB₀ - Pin 11: PB₁ - Pin 12: PB₂ - Pin 13: PB₃ - Pin 14: PB₄ - Pin 15: PB₅ - Pin 16: PB₆ - Pin 17: PB₇ - Pin 18: CB₁ - Pin 19: CB₂ - Pin 20: V_{CC} - Pin 21: IRQ - Pin 22: R/W - Pin 23: CS2 - Pin 24: CS1 - Pin 25: $\phi 2$ - Pin 26: D₇ - Pin 27: D₆ - Pin 28: D₅ - Pin 29: D₄ - Pin 30: D₃ - Pin 31: D₂ - Pin 32: D₁ - Pin 33: D₀ - Pin 34: RES - Pin 35: RS3 - Pin 36: RS2 - Pin 37: RS1 - Pin 38: RS0 - Pin 39: CA₂ - Pin 40: CA₁ R6522 Pin Configuration	Pin configuration for 6532 RIOT – RAM-IO-Timer: - Pin 1: V_{SS} - Pin 2: A₅ - Pin 3: A₄ - Pin 4: A₃ - Pin 5: A₂ - Pin 6: A₁ - Pin 7: A₀ - Pin 8: PA₀ - Pin 9: PA₁ - Pin 10: PA₂ - Pin 11: PA₃ - Pin 12: PA₄ - Pin 13: PA₅ - Pin 14: PA₆ - Pin 15: PA₇ - Pin 16: PB₇ - Pin 17: PB₆ - Pin 18: PB₅ - Pin 19: PB₄ - Pin 20: V_{CC} - Pin 21: PB₃ - Pin 22: PB₂ - Pin 23: PB₁ - Pin 24: PB₀ - Pin 25: IRQ - Pin 26: D₇ - Pin 27: D₆ - Pin 28: D₅ - Pin 29: D₄ - Pin 30: D₃ - Pin 31: D₂ - Pin 32: D₁ - Pin 33: D₀ - Pin 34: RES - Pin 35: R/W - Pin 36: RS - Pin 37: CS2 - Pin 38: CS1 - Pin 39: $\phi 2$ - Pin 40: A₆



PROZESSOREN 8 BIT – Teil 3

6800	8051
MOTOROLA 8 Bit CPU	Microcontroller 8 Bit
8048	TMS1100
Microcontroller 8 Bit	TI Microcontroller 4 Bit



PROZESSOREN 16 BIT

68000	TMS9900
MOTOROLA 16 Bit CPU	TI 16 Bit CPU
D₄ 1 D₃ 2 D₂ 3 D₁ 4 D₀ 5 AS 6 UDS 7 LDS 8 R/W 9 DTACK 10 BG 11 BGACK 12 BR 13 V_{CC} 14 CLK 15 GND 16 HALT 17 Reset 18 VMA 19 E 20 VPA 21 BERR 22 IPL2 23 IPL1 24 IPL0 25 FC2 26 FC1 27 FC0 28 A₁ 29 A₂ 30 A₃ 31 A₄ 32 64 D₅ 63 D₆ 62 D₇ 61 D₈ 60 D₉ 59 D₁₀ 58 D₁₁ 57 D₁₂ 56 D₁₃ 55 D₁₄ 54 D₁₅ 53 GND 52 A₂₃ 51 A₂₂ 50 A₂₁ 49 V_{CC} 48 A₂₀ 47 A₁₉ 46 A₁₈ 45 A₁₇ 44 A₁₆ 43 A₁₅ 42 A₁₄ 41 A₁₃ 40 A₁₂ 39 A₁₁ 38 A₁₀ 37 A₉ 36 A₈ 35 A₇ 34 A₆ 33 A₅	V_{BB} 1 V_{CC} 2 Wait 3 Load 4 HOLDA 5 Reset 6 IAQ 7 Phase 1 8 Phase 2 9 A₁₄ 10 A₁₃ 11 A₁₂ 12 A₁₁ 13 A₁₀ 14 A₉ 15 A₈ 16 A₇ 17 A₆ 18 A₅ 19 A₄ 20 A₃ 21 A₂ 22 A₁ 23 A₀ 24 Phase 4 25 V_{SS} 26 V_{DD} 27 Phase 3 28 DBIN 29 CRUOUT 30 CRUIN 31 INTREQ 32 64 Hold 63 MEMEN 62 Ready 61 WE 60 CRUCLK 59 V_{CC} 58 Not connected 57 Not connected 56 D₁₅ 55 D₁₄ 54 D₁₃ 53 D₁₂ 52 D₁₁ 51 D₁₀ 50 D₉ 49 D₈ 48 D₇ 47 D₆ 46 D₅ 45 D₄ 44 D₃ 43 D₂ 42 D₁ 41 D₀ 40 V_{SS} 39 Not connected 38 Not connected 37 Not connected 36 IC₀ 35 IC₁ 34 IC₂ 33 IC₃

Quelle: <http://www.cpu-world.com/info/Pinouts/>



PROZESSOREN Sonstige

Z8002	6809
ZILOG Z8000 16-Bit CPU	Motorola 8-Bit CPU
Pinout diagram for Z8002 (ZILOG Z8000 16-Bit CPU). The chip has 40 pins. The left side (pins 1-20) includes: AD₉, AD₁₀, AD₁₁, AD₁₂, AD₁₃, STOP, μ, AD₁₅, AD₁₄, V_{CC}, V_i, NV_i, NMI, RESET, μO, MREQ, DS, ST₃, ST₂, ST₁. The right side (pins 21-40) includes: AD₀, AD₈, AD₇, AD₆, AD₄, AD₅, AD₃, AD₂, AD₁, GND, CLK, AS, (DO NOT USE), B/W, N/S, R/W, BUSAK, WAIT, BUSRQ, ST₀.	Pinout diagram for 6809 (Motorola 8-Bit CPU). The chip has 40 pins. The left side (pins 1-20) includes: VSS, NMI, TRQ, FIRQ, BS, BA, VCC, A0, A1, A2, A3, A4, A5, A6, A7, A8, A9, A10, A11, A12. The right side (pins 21-40) includes: HALT, XTAL, EXTAL, RESET, MRDY, Q, E, DMA/BREQ, R/W, D0, D1, D2, D3, D4, D5, D6, D7, A15, A14, A13.
HD46505	2C03B
CRT Controller	PAL Grafik Controller
Pinout diagram for HD46505 (CRT Controller). The chip has 40 pins. The left side (pins 1-20) includes: V_{ss}, RES, LPSTB, MA₀, MA₁, MA₂, MA₃, MA₄, MA₅, MA₆, MA₇, MA₈, MA₉, MA₁₀, MA₁₁, MA₁₂, MA₁₃, DISPTMG, CUDISP, V_{cc}. The right side (pins 21-40) includes: VSYNC, HSYNC, RA₀, RA₁, RA₂, RA₃, RA₄, D₀, D₁, D₂, D₃, D₄, D₅, D₆, D₇, CS, RS, E, R/W, CLK.	Pinout diagram for 2C03B (PAL Grafik Controller). The chip has 40 pins. The left side (pins 1-20) includes: DBE, RST, PA13, PA9, AD7, AD6, AD5, AD4, AD3, AD2, AD1, AD0, ALE, RD, WE. The right side (pins 21-40) includes: INT, R/W, RS2, D7, D6, D5, D4, D3, D2, D1, D0, CLK, R, G, B, SYNC, GND, E, 40.



Audio Generatoren – Teil 1

AY-3-8910 / YM2149	YM2151
Audio Generator + Port Top View Pinout details for AY-3-8910: 1: Vss (GND) 2: N.C. 3: ANALOG CHANNEL B 4: ANALOG CHANNEL A 5: N.C. 6: IOB7 7: IOB6 8: IOB5 9: IOB4 10: IOB3 11: IOB2 12: IOB1 13: IOB0 14: IOA7 15: IOA6 16: IOA5 17: IOA4 18: IOA3 19: IOA2 20: IOA1 21: IOA0 22: CLOCK 23: RESET 24: A9 25: A8 26: TEST 2 27: BDIR 28: BC2 29: BC1 30: DA7 31: DA6 32: DA5 33: DA4 34: DA3 35: DA2 36: DA1 37: DA0 38: ANALOG CHANNEL C 39: TEST 1 40: Vcc (+5V)	FM Sound Generator • Top View Pinout details for YM2151: 1: Vss(GND) 2: IRQ 3: IC 4: A0 5: WR 6: RD 7: CS 8: CT1 9: CT2 10: D0 11: Vss (GND) 12: D1 13: D2 14: D3 15: D4 16: D5 17: D6 18: D7 19: SH2 20: SH1 21: SO 22: Vcc (+5V) 23: 01 24: 0M
YM3012	MSM5205
Stereo DAC Pinout details for YM3012: 1: Vcc 2: CLOCK 3: DIG GND(Vss) 4: S D (DATA) 5: SAM2 6: SAM1 7: ICL 8: ANALOG GND(Vss) 9: ANALOG GND(Vss) 10: CH-1 11: Vout 12: CH-2 13: COM 14: (ANALOG IN) 15: ToBUFF 16: MP 17: BC 18: Rb 19: ANALOG GND(Vss)	Speech Synthesizer Pinout details for MSM5205: 1: S1 2: S2 3: 4B/3B 4: D0 5: D1 6: D2 7: D3 8: NC 9: VSS 10: DAOUT 11: NC 12: T1 13: T2 14: VCK 15: RESET 16: XT 17: XT 18: VDD
SN76489	MSM6295
Sound Generator Pinout details for SN76489: 1: D2 2: D1 3: D0 4: READY 5: WE 6: OE 7: AUDIO OUT 8: GND 9: N.C. 10: D7 11: D6 12: D5 13: D4 14: CLOCK 15: D3 16: Vcc	Voice Synthesis IC Pinout details for MSM6295: 1: VSS 2: RD 3: WR 4: CS 5: XT 6: XT 7: SS 8: RESET 9: D0 10: D1 11: D2 12: D3 13: D4 14: D5 15: D6 16: D7 17: VDD 18: A0 19: A1 20: A2 21: A3 22: A4 23: A5 24: A6 25: A7 26: A8 27: A9 28: A10 29: A11 30: A12 31: A13 32: A14 33: A15 34: A16 35: A17 36: DAO 37: I0 38: I1 39: I2 40: I3 41: I4 42: I5 43: I6 44: I7

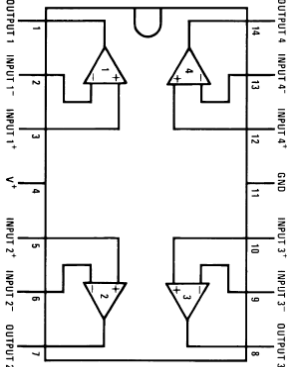
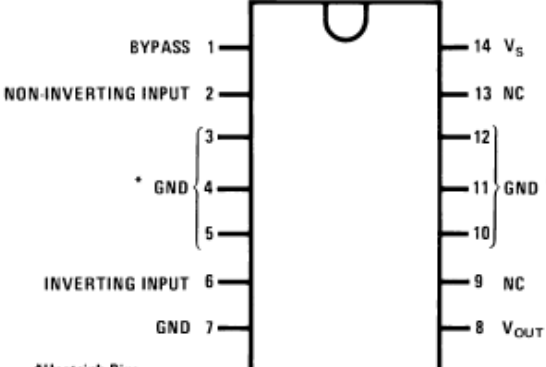
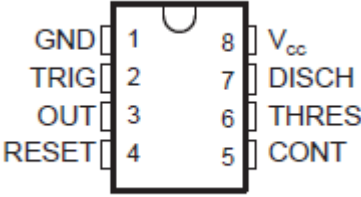
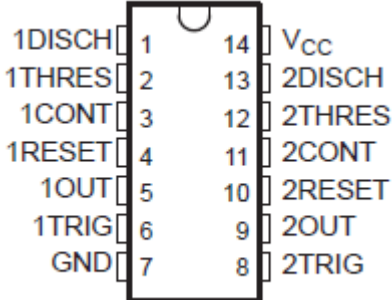
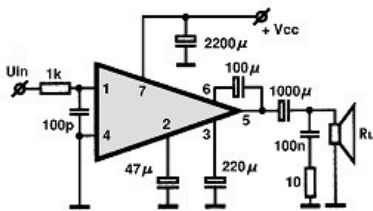
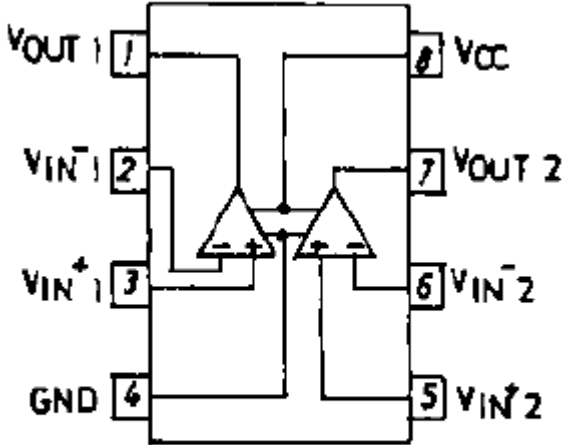


Audio Generatoren – Teil 2

YM3014B		YM2203	
Serial D/A Converter		FM Operator	

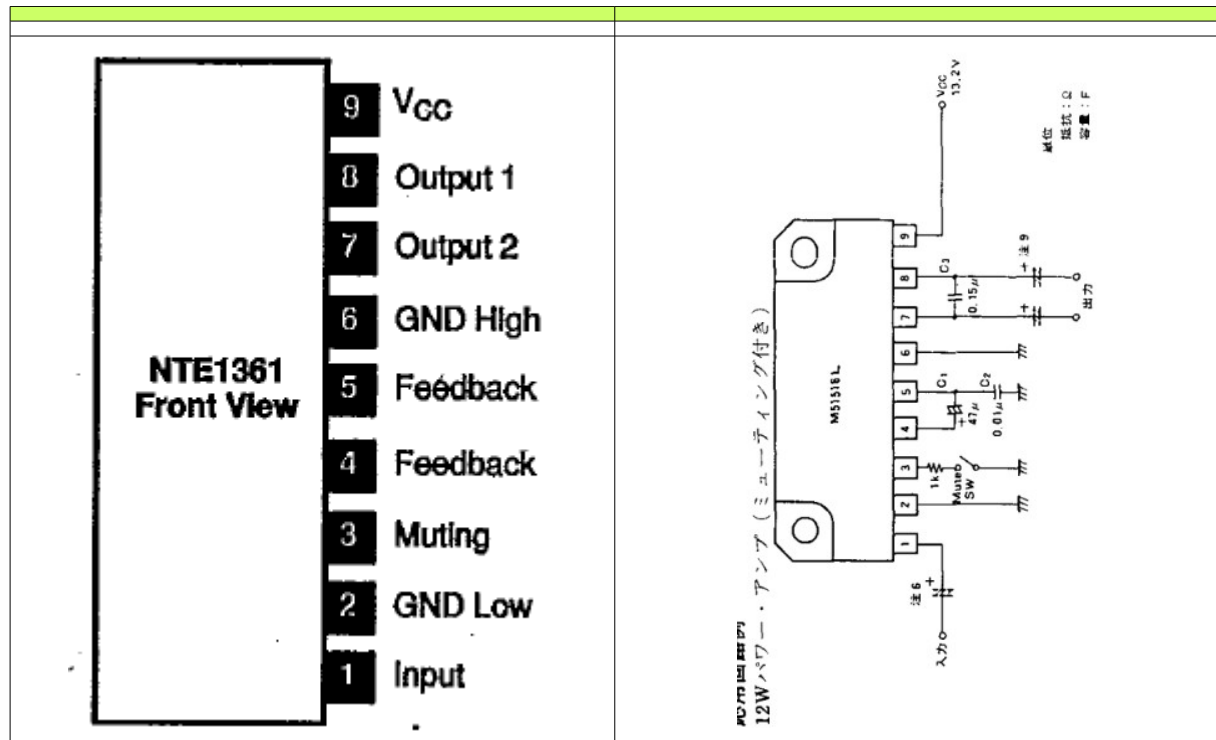


Analog ICs und Audio-Verstärker

LM324 / TL084 / LF347	LM380
Quad OP-AMP	Amp 2,5W
	 *Heatsink Pins
NE555	NE556
Timer / Multivibrator	Dual Timer / Multivibrator
	
μPC1181/1182	LA6358
12V 9W	Dual-OPAMP
 μPC1182H REVERSE PINNING	 LA6358N

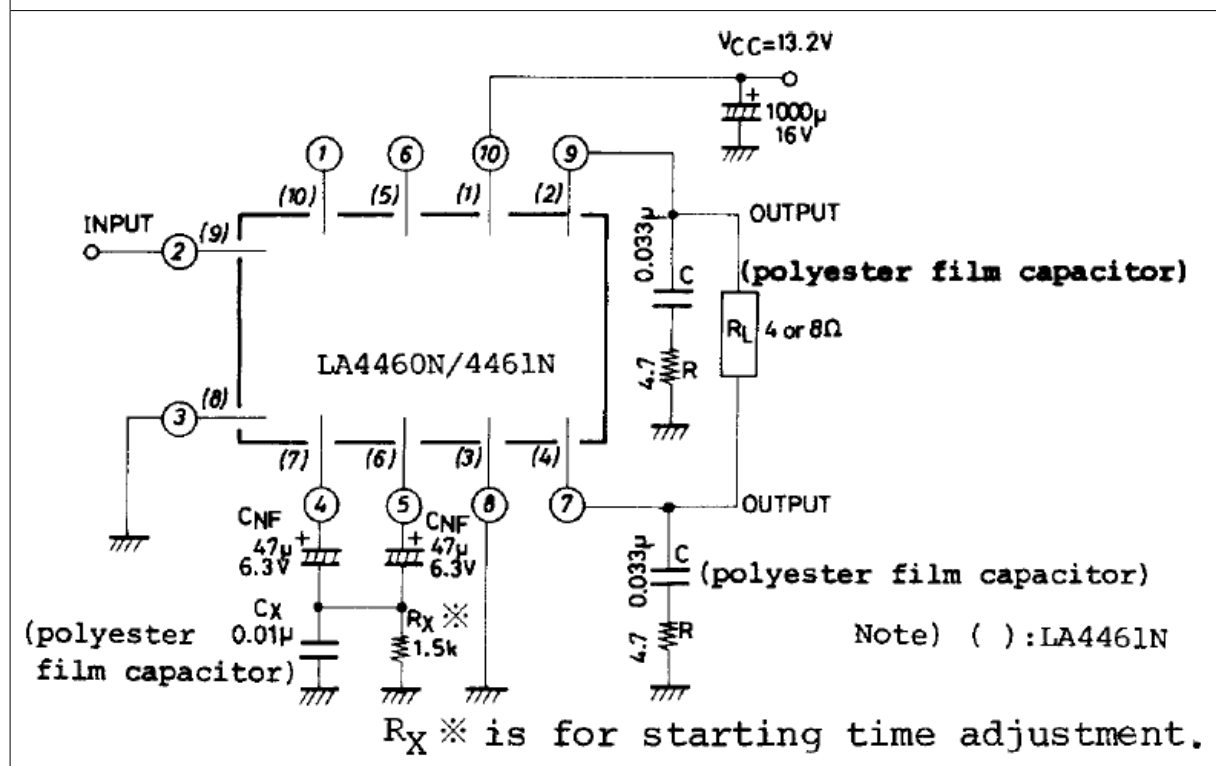


Analog ICs und Audio-Verstärker – Teil 2



LA4460/4461

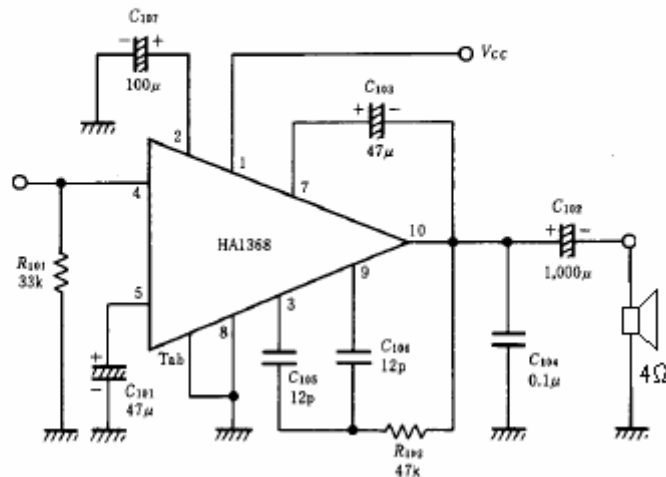
12W max. 18V





HA 1368 = Standard Pinout

HA 1368/R = Reverse Pinout



TDA1510

24W Bridge Amplifier

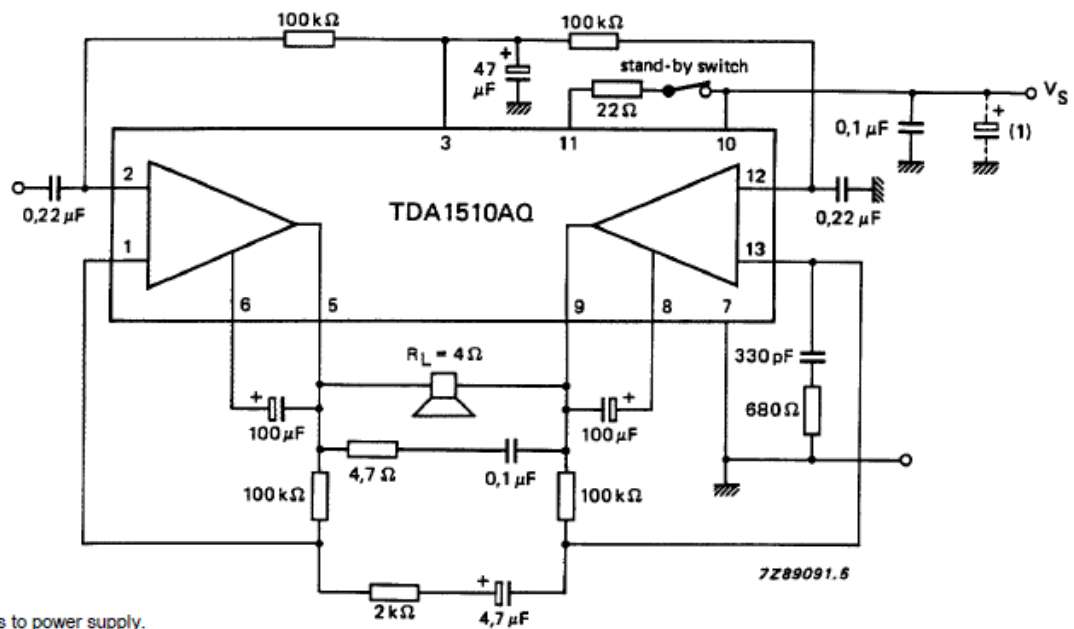


Fig.3 Test and application circuit; Bridge Tied Load (BTL).



Analog ICs und Audio-Verstärker – Teil 4

LM3900	
Quad OPAMP 5V	



Optoelektronik

PS2401-4	PC817
Quad Photocoupler	Single Photocoupler
H22A	
Gabellichtschranke	
Top View	

Sonstige Elektronik

MB3773	
Reset-Controller	